



AD8420 Data Sheet Limit Change

Data Sheet Specification Comparison

Rev A

Data Sheet	AD8420
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+V_S = +15 V, -V_S = -15 V, V_{REF} = 0 V, T_A = 25°C, G = 1 to 1000, R_L = 20 kΩ, specifications referred to input, unless otherwise noted.

Table 3.

Parameter	Test Conditions/Comments	Min	Typ	Max	Unit
COMMON-MODE REJECTION RATIO (CMRR)	V _{CM} = -10 V to +10 V				
CMRR DC to 60 Hz		92			dB
CMRR at 1 kHz		92			dB
NOISE					
Voltage Noise					
Spectral Density	f = 1 kHz, V _{DIFF} ≤ 100 mV		55		nV/√Hz
Peak to Peak	f = 0.1 Hz to 10 Hz, V _{DIFF} ≤ 100 mV		1.5		μV p-p
Current Noise					
Spectral Density	f = 1 kHz		80		fA/√Hz
Peak to Peak	f = 0.1 Hz to 10 Hz		3		pA p-p
VOLTAGE OFFSET					
Offset	V _S = ±15 V ¹			250	μV
Average Temperature Coefficient	T _A = -40°C to +85°C			1	μV/°C
Offset RTI vs. Supply (PSR)	V _S = ±15 V	100			dB
INPUTS	Valid for REF and FB pair, as well as +IN and -IN				
Input Bias Current ²	T _A = +25°C		20	27	nA
	T _A = +85°C			24	nA

Rev B

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